

CLAIMS

What is claimed is:

1. A circuit comprising:

a differential amplifier having a differential input terminal pair and a differential output terminal pair, wherein the differential amplifier provides a differential oscillating signal at the differential output terminal pair; and

an inductor-capacitor (LC) tank coupled between the differential input and output terminal pairs, wherein the LC tank comprises an inductive element coupled in parallel with a capacitive element, wherein the capacitive element comprises:

a first varactor pair coupled to receive a first differential control voltage, the first control voltage i) sets a capacitance of each varactor of the first varactor pair and ii) provides a first level of adjustment to an oscillation frequency of the oscillating signal, and

a second varactor pair coupled to receive a second differential control voltage, the second control voltage i) sets a capacitance of each varactor of the second varactor pair and ii) provides a second level of adjustment to the oscillation frequency of the oscillating signal, wherein the first and second levels of adjustment are different.

2. The invention as recited in claim 1, wherein the capacitive element is AC-coupled between the differential input and output terminal pairs.

3. The invention as recited in claim 1, wherein the differential amplifier comprises a set of cross-coupled transistors.

4. The invention as recited in claim 3, wherein the set of cross-coupled transistors is configured as a pair of back-to-back inverters.

5. The invention as recited in claim 1, wherein each of the first and second pairs of varactors are configured as back-to-back varactors.

6. The invention as recited in claim 1, wherein the circuit is a voltage-controlled oscillator (VCO).

1 7. The invention as recited in claim 6, wherein the VCO is employed in a phase-
2 locked loop (PLL) circuit, the first differential control voltage represents a feedback error for
3 process variations of the PLL circuit, and the second differential control voltage represents a
4 feedback phase error of the PLL circuit.

1 8. The invention as recited in claim 1, further comprising at least one other pair of
2 varactors, each of the at least one other pair of varactors coupled to receive a corresponding
3 differential control voltage to i) set a capacitance of each varactor of the at least one other
4 varactor pair and ii) provide a corresponding level of adjustment to the oscillation frequency of
5 the oscillating signal.

1 9. The invention as recited in claim 1, further comprising a filter, coupled between a
2 source voltage and the differential output terminal pair of the differential amplifier, the filter
3 adapted to filter one or more harmonics of the oscillation frequency.

1 10. The invention as recited in claim 1, wherein the circuit is embodied in an
2 integrated circuit.